



11 Publication number:

0 545 042 A1

(12)

EUROPEAN PATENT APPLICATION

(21) Application number: 92117506.3

(51) Int. Cl.5: H02H 9/00, G01R 31/36

2 Date of filing: 14.10.92

Priority: 29.11.91 US 799811

(3) Date of publication of application: 09.06.93 Bulletin 93/23

Designated Contracting States:
 AT BE CH DE DK ES FR GB GR IT LI LU MC
 NL PT SE

Applicant: MOTOROLA, INC. 1303 East Algonquin Road Schaumburg, IL 60196(US)

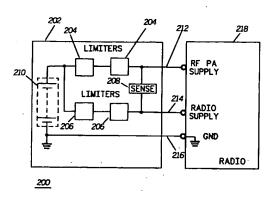
Inventor: Bishop, Michael R. 61 S.W. 91 Avenue No. 205 Plantation, Florida 33324(US) Inventor: Minasi, David H. 1150 N.W. 111 Avenue Plantation, Florida 33322(US)

Representative: Dunlop, Hugh Christopher et al Motorola European Intellectual Property Operations Jays Close Vlables Industrial Estate Basingstoke, Hampshire RG22 4PD (GB)

(54) Dual output battery with fault detect.

The battery of Fig. 3, includes a first redundant limiting circuit (302) for limiting the voltage and current levels which are outputted by the first limited battery output (308). A second redundant limiting circuit (304) provides for a second limited battery output (310). Connected to both the first (308) and second (310) battery outputs is a fault monitoring circuit (306), which monitors for any faults such as electrical shorts, occurring at any of the two battery outputs (308 or 310). The fault monitoring circuit (306) interrupts the appropriate output, if a fault, or a current limiting condition occurs.

FIG.2



25

30

45

50

55



Technical Field

This invention relates generally to batteries, and more specifically to a portable battery for use with a communication device.

Background

The high circuit densities encountered in modern radios make it difficult for radio manufacturers to meet the spacing specifications (intrinsic safety spacing requirements) laid down by intrinsic safety testing agencies (e.g., Underwriters' Laboratories (UL) -United States, Factory Mutual (FM) - United States, BASEEFA - United Kingdom Testing Agency, CENTALEC- Federal Republic of Germany, etc.) for achieving intrinsic safety approval ratings. Both component-to-component and layer-to-layer spacing distances, have to be well above the specified minimum spacing distances, (typically have to be greater than 0.5 millimetres) in order to meet the spacing constraints placed on radio manufacturers by intrinsic safety testing agencies.

The spacing requirements imposed by the testing agencies (in order to meet intrinsic safety approvals) are seldom achieved by radio manufacturers, due to the great amount of integration modern radios are required to have in order to achieve their small size and at the same time still retain all of their numerous user features. Not meeting the minimum spacing requirements causes the testing agencies to consider the whole radio as a "mass fault." This allows the testing agency to sum all capacitances and/or inductances and place them as if they are connected to the test point (node) they are testing in the radio. They can also assume the worst case voltage and current levels, and also assume the greatest power output capable of being achieved by the radio, when running any approval tests. This in turn forces the radio manufacturers to limit current and voltage levels in the radio to levels considered safe enough to meet intrinsic safety requirements by intrinsic safety testing agencies.

Typical maximum values imposed by the agencies, once a radio is considered a mass fault, are in the order of a maximum operating voltage of six volts and no more than a maximum of 250 milliamps current output at the battery. For more stringent approval ratings, not more than 50 milliamps of current can be sourced by the battery. The main problem with limiting a radio to these low current and voltage values, is that these values limit the total amount of power output the radio would be capable of delivering. These very restrictive value settings would not allow high powered radios to meet intrinsic safety levels.

In FIG.1, there is shown a block diagram of a prior art portable radio with battery 100. Battery

102 consists of a plurality of rechargeable nickelcadmium battery cells connected in series, forming a rechargeable battery pack 108. Coupled to the positive terminal 114 of battery pack 108, are a redundant set of current and voltage limiter circuits 104. The output 112 of the voltage/current limiter circuits, provides a regulated output for portable radio 106. A second output 110 is provided by battery 102, which provides the ground potential connection to radio 106. The major problem with the prior art radio and battery 100 shown in FIG. 1, is that radio 106 would still be limited to the low voltage and current levels (e.g, 6 volts, 250 milliamps) required by the testing agency in the case the spacing requirements in the radio 106 are not being meet.

A need thus exists in the art for a way of achieving higher levels of intrinsic safety standards in portable electronic devices without having to meet the spacing limitations placed on the devices by the testing agencies, or without having to limit the amount of voltage and current capable of being supplied by the battery to the device in order to meet the requirement.

Summary of the Invention

Briefly according to the invention, there is provided a battery comprising a battery housing and a battery pack located inside of the battery housing. The battery further includes first and second limiting means coupled to the battery pack for providing first and second limited battery outputs, respectively. Finally, the battery includes a fault detection means coupled to the first and second limited battery outputs for interrupting at lest one of the first and second limited battery outputs in the event a fault condition occurs at any of the two limited battery outputs.

The battery monitors whether a fault condition has occurred and automatically interrupts the outputs to the radio. Thus, isolation or spacing requirements within the radio would not have to be met in order to achieve intrinsic safety approval requirements.

An exemplary embodiment of the invention will now be described with reference to the accompanying drawings.

Brief Description of the Drawings

FIG. 1 is a block diagram of a prior art battery and radio.

FIG. 2 is a block diagram of a battery and radio in accordance with the present invention.

FIG. 3 is an electrical schematic of the battery of FIG. 2.

5



Detailed Description of the Preferred Embodiment

Referring now to the drawings and in particular to FIG. 2, there is shown a dual output battery and radio 200. The battery includes a battery housing 202 which encloses a battery pack 210. Battery pack 210 includes a plurality of rechargeable nickel-cadmium battery cells connected in series. Connected to the positive terminal of battery pack in parallel, are a first and second limiting means such as a set of redundant voltage/current limiting circuits 204 and 206.

The first redundant limiting circuit 204, provides for a first limited battery output 212, while the second redundant limiting circuit 206 provides for a second limited battery output 214. In the preferred embodiment, the first limited battery output 212 is electrically connected to the R.F. power amplifier section of radio 218 and to other high current consuming circuits. The first output 212 preferably provides for a higher current and voltage level settings, given the fact that the R.F. Power amplifier section draws the most amount of power of any of the other circuits in radio 218 when it is in operation. The second limited output 214, is connected to the remaining electrical circuits of radio 218, which include all of the radio frequency and control circuits (low current consumption circuits) associated with a typical two-way radio. The final output provided by the battery is a ground potential output 216 which is coupled to the radio's ground potential (usually the radio chassis). Radio 218 includes both a conventional transmitter and receiver circuit as known in the art.

Coupled between the first and second (204 and 206) redundant limiting circuits at the output side, is a fault detection means (sense means) 208. The fault detection circuit 208 interrupts one or more of the battery outputs in the event a fault condition (e.g., a short circuit in radio 218, etc.) occurs at one or more of the battery outputs 212 and 214. The operation of the fault detection means 208 will be discussed in detail below.

In FIG. 3, an electrical schematic of battery of FIG. 2 is shown. Battery 300, is composed of three major blocks, two redundant current/voltage limiter circuits 302 and 304, and a fault monitoring circuit 306. Each of the three major blocks 302, 304 and 306, include redundant elements (electronic components) for added safety of operation in hazardous environments. The redundancy allows for battery 300 to continue to provide fault monitoring and limiting capabilities, in the event an electrical component should happen to fail in the battery. The typical operation of the circuits will be explained below. Note that the redundant components will be shown in brackets after any of the discussions,

given that the redundant component has the same operational characteristics as the primary component.

Preferably, first limiter circuit 302 provides for a high current and voltage output (VS1) 308, while second limiter circuit 304 provides for a lower current and voltage output (VS2) 310. The redundant fault monitoring (detection) circuit 306 provides two major functions to battery 300. The first major function provided by fault monitoring circuit 306, is that it interrupts (shuts off) the VS1 output (high current supply) 308 in the event that a fault shorting outputs VS 1 308 and VS2 310 together occurs. Secondly, fault monitoring circuit 306 shuts off output VS1 308, in the event that output VS1 308 remains in a current limited state for an extended period of time. The predetermined length of time the fault monitoring circuit 306 will allow the VS1 output 308 to remain in a current limiting condition prior to interrupting (shutting off) the battery output, is determined by the combination of capacitor C2 (C1) and resistor R17 (R20). This prevents excessive heating of current limiting Fieldeffect transistors (FET's) Q3 and Q4 in the high current output side of battery 300. This interruption is not necessary in the preferred embodiment for FET's Q17 and Q18 in circuit 304, as the limiting current setting for output VS2 310 is set lower than that of output VS1 308 and thereby, very limited heat build up is generated at circuit 304.

The theory of operation for first and second limiter circuits 302 and 304 respectively, will now be discussed. The gate terminals of transistors Q3 and Q4, of first limiter circuit 302, are pulled "low " (towards ground potential) by a series pair of resistors R6 and R7. This allows for transistors Q3 and Q4 to be in the saturated ("on") state. Current supplied by output VS1 308 must pass through resistor R1, which creates a voltage drop across R1 which is proportional to the current sourced by output VS1 308. When this voltage becomes large enough to forward bias the base-emitter junction of transistor Q1 (or redundant transistor Q2), the voltages at the gate terminals of transistors Q3 and Q4 will be pulled up, resulting in an increase in channel resistance in transistors Q3 and Q4, which limits the current output of output VS1 308. This in turn results in the conduction of transistor Q1 (or Q2). Thus, the current limit setting is determined by the value selected for resistor R1 or resistor R22 for circuit 304.

The voltage limiting of circuit 302 occurs when voltage at output VS1 308 is sufficient to cause current to flow through diode CR1 (or CR2 if utilizing the redundant circuit) into the base of transistor Q6 (Q8 of the redundant circuit). Transistor Q5 (or redundant transistor Q7) will then go into conduction which pulls the voltage at the gate terminal of

15

30

35

40

45

transistor Q3 and Q4 high. This limits the voltage at output VS1 308. The voltage limit setting is determined by the choice of diode CR1 (CR2 in the redundant circuit).

The second voltage/current limiter circuit 304 operation is identical to the above circuit description, except for the choice of current and voltage limit settings. The voltage setting is decided by the choice of diode CR3 (redundant diode CR4) and the current limiting setting is decided by the value chosen for resistor R22. In second limiting circuit 304, the current limiting threshold is preferably set at a lower value in the preferred embodiment, since it is providing current for a portion of radio 218 which consumes a minimal amount of current (typically under 250 milliamps).

In the preferred embodiment (under normal conditions), the voltage at output VS1 308 is greater than that at output VS2 due to the choice of voltage/current limit settings. As such, base current flows in to transistor Q14 (Q11 of the redundant circuit) of fault detection circuit 306 which prevents conduction of transistors Q13 and Q12 (transistors Q10 and Q9, respectively of the redundant circuit). As long as transistor Q12 (Q9) is not conducting, the fault monitor circuit 306 does not influence the operation of the first current/voltage limiter circuit 302.

Any condition that results in the reduction of output VS1 308 or increase of output VS2 310 such that [VS1-VS2] < VBE (approximately .6 volts) results in loss of base current to transistor Q11 (Q14 in the redundant circuit). This in turn causes conduction in transistor Q10 (Q13) and transistor Q9 (Q12). When transistor Q9 (Q12) conducts, the gate terminals of transistors Q3 and Q4 are pulled up, cutting off supply to output 308. Output VS1 308 will remain shut off until the battery is removed from the radio (not shown) which causes switch S1 314 to open and thereby allows for the circuits to reset. In order to reset switch 314, the radio user must place back the battery on the radio 218 after the short which caused the interruption has been resolved. Switch 134 can be a conventional plunger type of switch which is normally open, and which becomes closed upon the plunger being depressed (battery being placed on the radio). Capacitor C2 (C1) causes the fault monitor circuit 306 to ignore transient dips in current or voltage which may occur in output VS1 308 which would otherwise cause the output(s) to be interrupted. FETs Q3, Q4, Q17 and Q18 are preferably P-channel enhancement mode TMOS field-effect transistors. Finally, battery 300 includes a third ground potential output 312 for coupling to radio 218.

Although the preferred embodiment has been limited to a specific configuration, as described by the preferred embodiment, those skilled in the art

can realize that many variations to the above battery circuit can be designed for. For example, more than two outputs can be provided by battery 300 as well as different current and voltage limiting criteria's can be set.

In summary, the present invention provides for a battery 300 for use with a portable electronic device such as a two-way portable radio 218 which can provide for fault monitoring capability. The battery 300 comprises two voltage/current limited outputs 308 and 310 and a fault monitoring circuit 306 coupled to a battery pack 316 all inside of the battery housing. The fault monitoring circuit 306 protects the radio in the case both outputs VS1 and VS2 become shorted together, or in the event one or more of the outputs remains current limited for a predetermined period of time, due to a fault in the radio or elsewhere. The invention allows for a radio and battery combination which can bypass the spacing and maximum voltage/current limits imposed by the testing agencies in order to be approved for intrinsic safety approval. Battery 300 guarantees that if any fault occurs at either of the battery outputs 308 and 310, the appropriate output(s) will be interrupted (shut off). The invention thereby allows for high powered radios which have not ordinarily been able to meet intrinsic safety levels, to now be able to achieve those approvals. Furthermore, the added redundancy of the circuits provides for an extra measure of safety for radios operating in very dangerous environments (e.g., gaseous, ignitable, etc.).

Claims

- 1. A battery, comprising:
 - a battery housing;
 - a battery pack located inside of the battery housing;

first limiting means coupled to the battery pack for providing a first limited battery output;

second limiting means coupled to the battery pack for providing a second limited battery output; and

a fault detection means coupled to the first and second battery outputs for interrupting at least one of the first and second limited battery outputs in the event a fault condition occurs at any of the two battery outputs.

- The battery of claim 1, wherein the fault detection means interrupts the first and second limited battery outputs when an electrical short is sensed between the first and second battery outputs.
- 3. The battery of claim 1 or 2, wherein the first and second limiting means provide both volt-

55



age and current limiting.

4. The battery of claim 1, 2 or 3, wherein the battery is a portable rechargeable battery.

5

10

- 5. A battery comprising:
 - a battery housing;
 - a battery pack located inside of the battery housing;

first limiting means coupled to the battery pack for providing a first battery output;

second limiting means coupled to the battery pack for providing a second battery output; and

fault detection means coupled to the first and second limited battery outputs for interrupting at least one of the first and second limited battery outputs in the event a current limiting condition occurs at any one of the two battery outputs.

20

- 6. The battery of claim 5, wherein the fault detection means does not interrupt one of the two battery outputs until the current limiting condition occurs for a predetermined period of time.
- 25
- The battery of claim 5 or 6, wherein the first and second limiting means provide both voltage and current limiting.

30

8. The battery of claim 5, 6 or 7, wherein the fault detection means interrupts the first and second limited battery outputs when an electrical short is sensed between the first and second limited battery outputs.

35

- 9. A radio comprising:
 - a transmitter;
 - a receiver coupled to the transmitter; and
 - a battery constructed in accordance with any preceding claim.

45

40

50

55

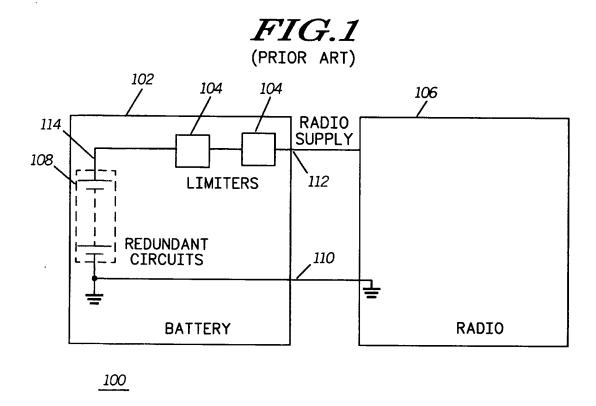
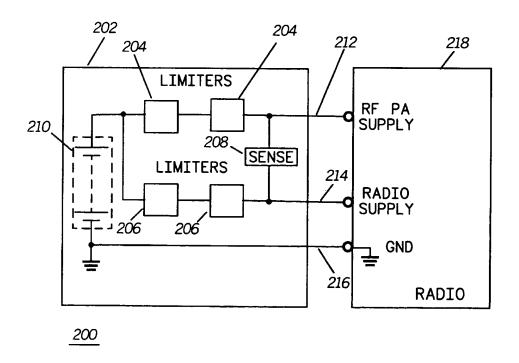
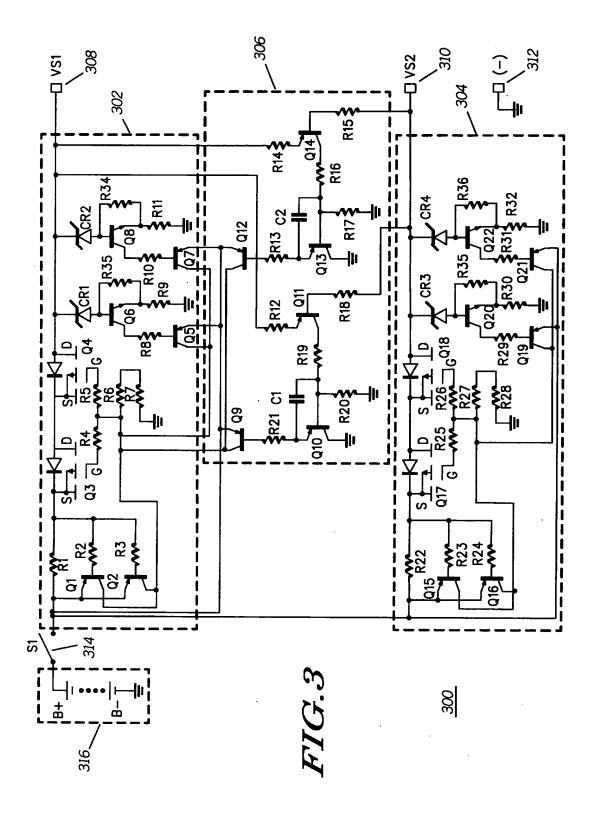


FIG.2







EUROPEAN SEARCH REPORT

Application Number

EP 92 11 7506

Category	Citation of document with indic of relevant passag		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
Y	DE-A-3 322 244 (BOSCH *abstract* * page 5 *		1-3,5,7	H02H9/00 G01R31/36
Y	DE-A-3 931 537 (SIEME * page 2, line 65, pa 2A,B *	NS) ragraph 67; figures	1-3,5,7	
A	EP-A-0 294 139 (MEASUI *absract*	REMENT TECHNOLOGY)	9	
A	US-A-4 412 265 (BUUCK))		
	- -			
			•	
				TECHNICAL FIELDS SEARCHED (Int. Cl.5)
				H02H G01R
l	The present search report has been	drawn up for all claims		
Place of search Date of completion of the search			<u>'</u>	Exeminer
THE HAGUE		18 FEBRUARY 1993	1993 HOORNAERT W.	
CATEGORY OF CITED DOCUMENTS X: particularly relevant if taken alone Y: particularly relevant if conshined with another document of the same category		E : earlier patent do after the filing d D : document cited i	T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons	
A: tech O: non	nological background -written disclosure rmediate document	& : member of the si document		

EPO PORM 1503 03.62 (P0401)